N 69-39462

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Date 15 September 1969

Revised
LIMITED REPORT
METALLOGRAPHIC EXAMINATION OF SOLDERING OF MULTILAYEP BOARDS FOR AIRLOCK
Report 061-015-76 Model Airlock
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PAGE	1
REPORT	061-015-76
MODEL	Airlock

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METALLOGRAPHIC EXAMINATION OF SOLDERING OF MULTILAYER BOARDS FOR AIRLOCK

The object of this test was to evaluate the solder connections of components on multilayer boards. This test became necessary when Electro-Mechanical Research (EMR) Co. requested a deviation from MDC. P.S. 22800 in soldering components on multilayer boards. EMR indicated that incomplete solder flow into the plated holes occurs because of the heat sink created by eleven copper layers, ground planes, and shields.

A multilayer board (P/N 52-203-403-80041) which had not been fully soldered was obtained from EMR, and various fluxes and soldering techniques were applied. A photograph of the as received multilayer board with arrows indicating connectors selected for examination is shown as Figure 1 on page 4. The conditions under which the connections were prepared and the visual results are shown in Table 1 on page 3.

The selected hole areas were sectioned and mounted for metallographic examination. Photomacrographs and photomicrographs of each of the examined hole areas are shown in Figures 2 through 12 pages 5 through 13. The following observations were made during the examination of the hole areas:

- 1. Connections C, DE, and F showed incomplete solder flow as shown in Figures 4, 5, and 6, respectively.
- 2. Hole "U" (designated as the unsoldered hole), which had been copper-tinlead plated but which did not have a solder connection, showed evidence
 of a void in the solder plate and a crack in the copper plate. This
 condition is shown in Figure 10 on page 13. Repolishing of the
 specimen confirmed that the copper plate was fractured 0.07 inch below
 the top face of the board, (Figure 11 on page 13).

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PAGE	2
REPORT	061-015-76
HODEL	Airlock

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3. Thickness measurements of the solder layer and the copper plate were made on the unsoldered hole from the photomicrograph in Figure 13 on page 13. The thicknesses were as follows: average copper plate-0.0024 inch, solder layer-0.0013 inch.

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PAGE	3	······································
REPORT	061-015-76)
MODEL	Airlock	

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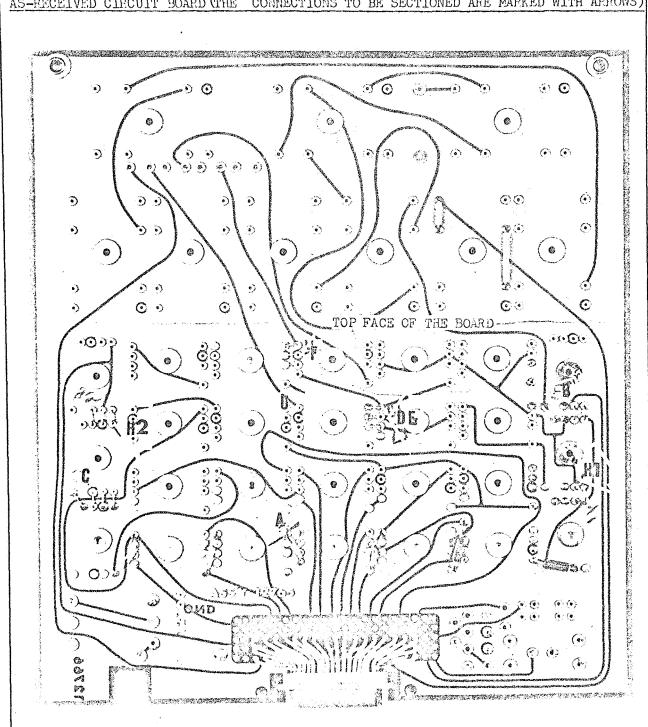
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PAGE	4
REPORT	061,-01.5-76
MODEL	Airlock

FIGURE 1

AS-RECEIVED CIRCUIT BOARD (THE CONNECTIONS TO BE SECTIONED ARE MARKED WITH APROVS)

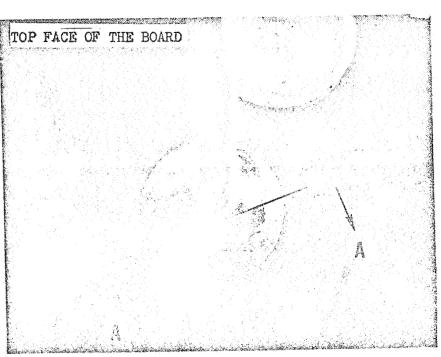


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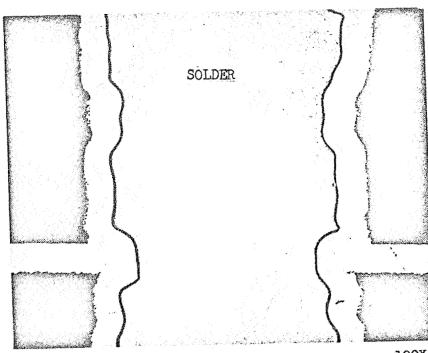
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CONNECTION A SHOW



CONNECTION A

16X



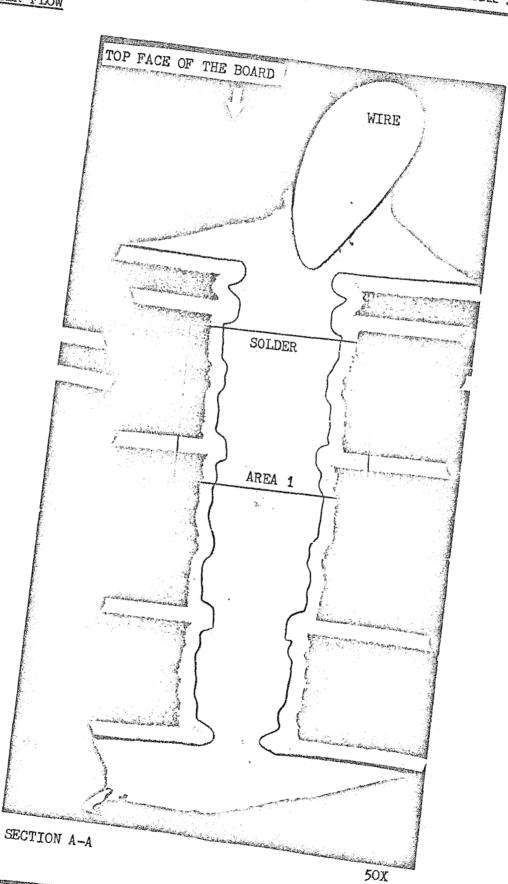
AREA 1 (SECTION A-A)

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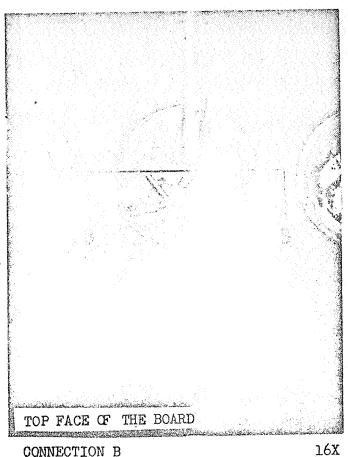
FIGURE 2

MODEL descriptions

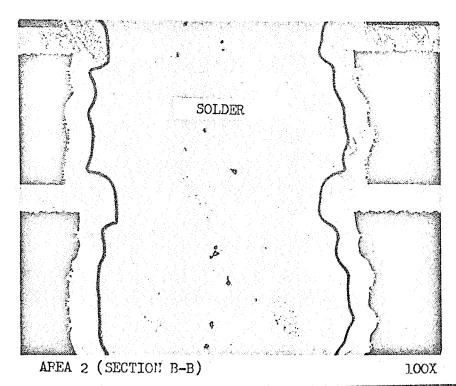
COMPLETE SOLDER FLOW



CONNECTION B SHOW



CONNECTION B



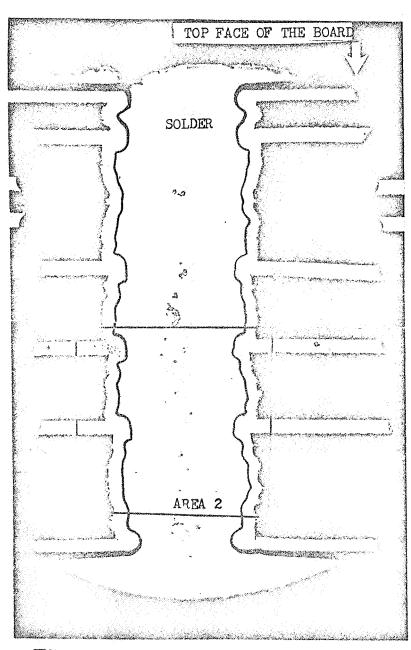
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PAGE _	6	
REPORT	061-015-76	
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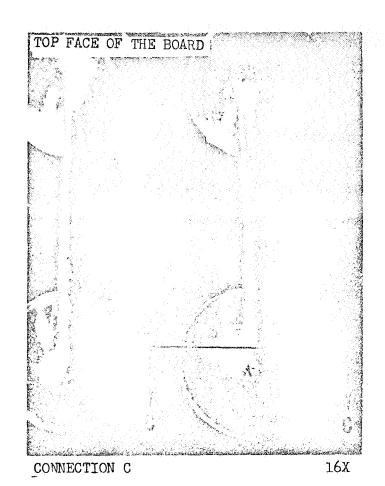
IG COMPLETE SOLDER FLOW

FIGURE 3



SECTION B-B

CONNECTION C SHOWT



WIRE

SOLDER

AREA 3 (SECTION C-C)

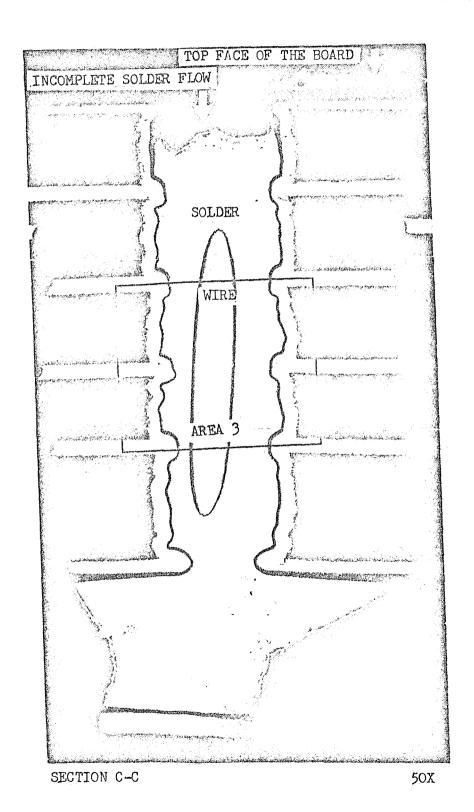
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PAGE	
REPORT	061-015-76
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INCOMPLETE SOLDER FLOW

FIGURE 4

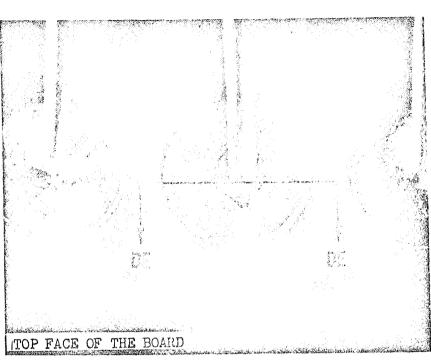


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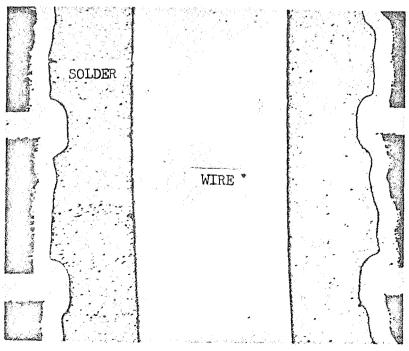
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CONNECTION DE SHOWIN



CONNECTION DE

16X



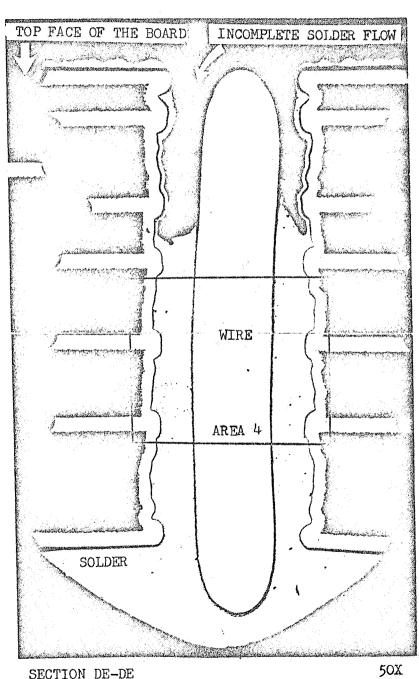
AREA 4 (SECTION DE-DE)

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PAGE	
REPORT	061-015-76
MODEL	Airlock

ICOMPLETE SOLDER FLOW

FIGURE 5

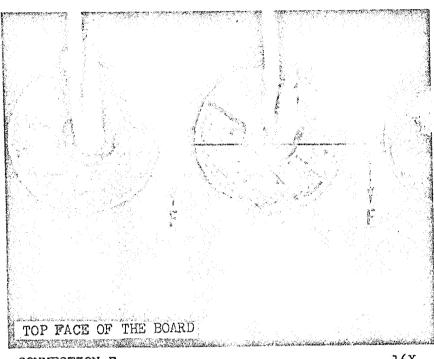


SECTION DE-DE

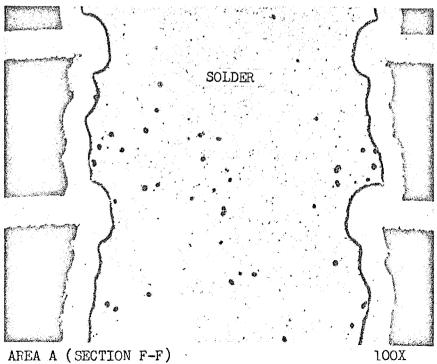
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CONNECTION F SHOWIN



CONNECTION F



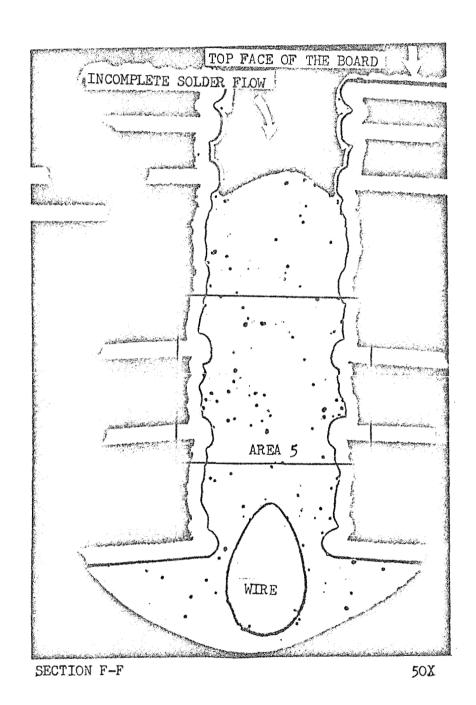
AREA A (SECTION F-F)

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PAGE	· 9	
REPORT	061-015-76	
MODEL	Airlock	

COMPLETE SOLDER FLOW

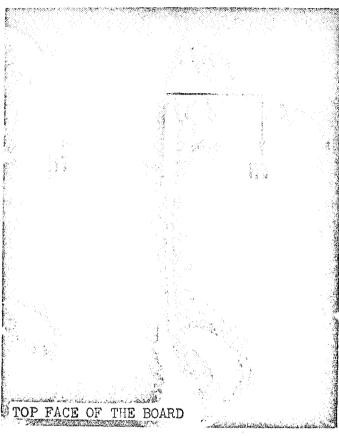
FIGURE 6



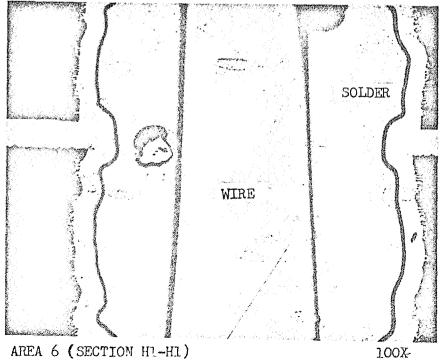
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CONNECTION HI

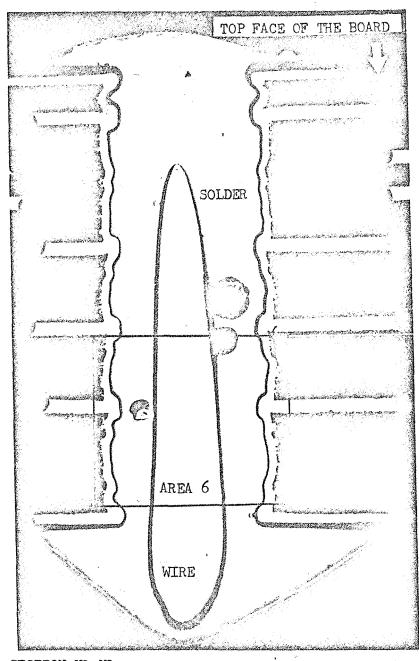


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PAGE	10
REPORT	061-015-76
MODEL	Airlock

OMPLETE SOLDER FLOW DS ALONG WIRE

FIGURE 7

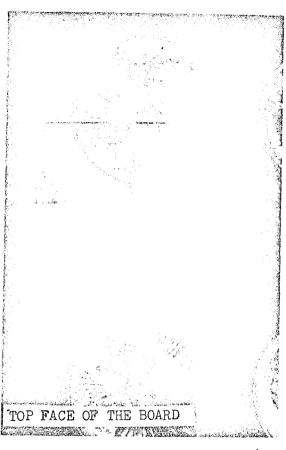


SECTION H1-H1

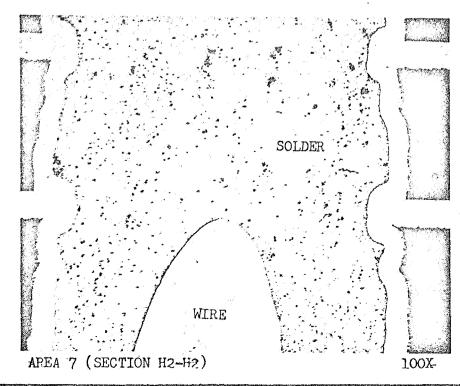
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CONNECTION HS SH



CONNECTION H2

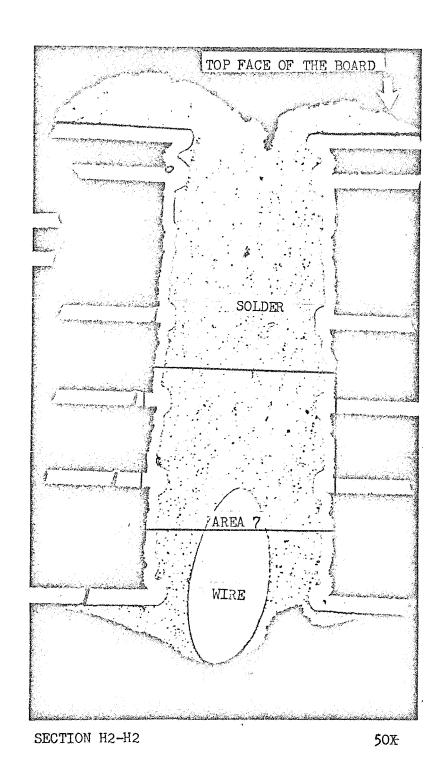




PAGE	11	
	061-015-76	
REPORT _	en karataran karatar	***
MODEL	Airlock	

FIGURE 8

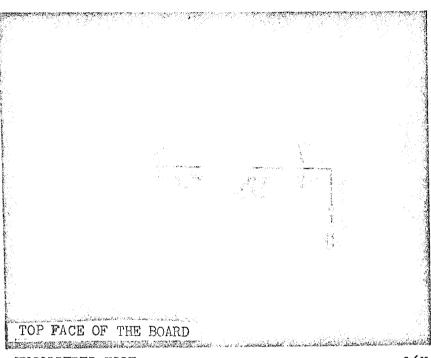
3 COMPLETE SOLDER FLOW



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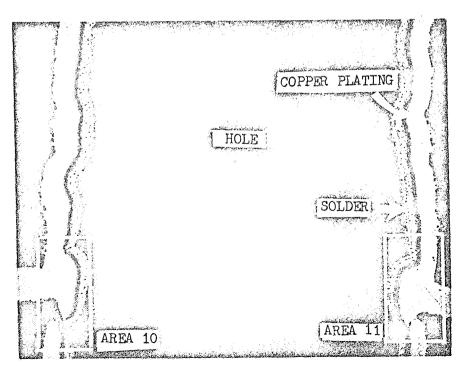
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UNSO:



UNSOLDERED HOLE

16X



AREA 8 (SECTION U-U)

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REPORT	061-015-76	
MODEL	Airlock	

ED HOLE

FIGURE 9

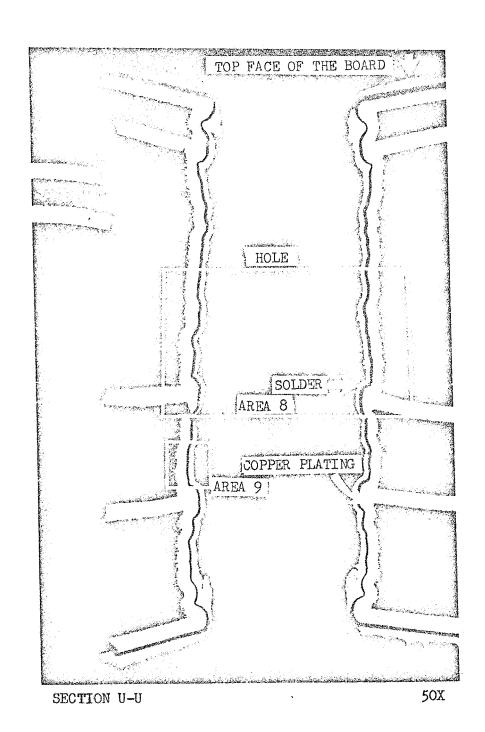
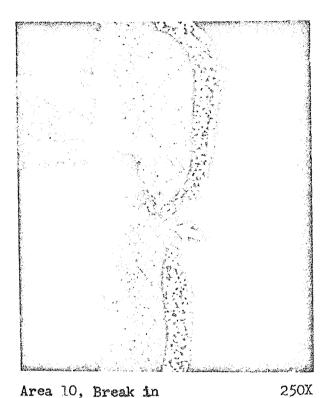
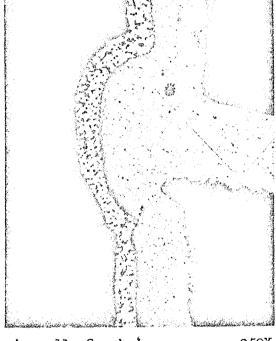


FIGURE 10 - AREAS 10 AND 11 OF SECTION U-U



Area 10, Break in Copper Plate (arrow)



Area 11, Crack in Copper Plate (arrow)

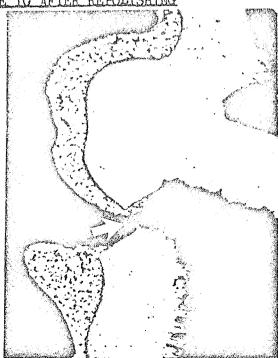
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FIGURE 11 SAME AREAS AS FIGURE 10 AFTER REPOLISHING



Area 10, Break in Copper Plate (arrow)





Area 11, Break In Copper Plate (arrow)



PAGE 13.
REPORT 061-015-76
MODEL Airlock

FIGURES 10, 11, 12

